

Electronic Acknowledgement Receipt

EFS ID:	6259462
Application Number:	10551324
International Application Number:	
Confirmation Number:	7460
Title of Invention:	SEMICONDUCTOR TRANSISTOR (DMOS) DEVICE FOR USE AS A POWER AMPLIFIER
First Named Inventor/Applicant Name:	Radjindrepersad Gajadharsing
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Attorney Docket Number:	SOROK16.001APC
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1	Notice of Appeal Filed	SOROK16-001APC_Notice_Of_Appeal.pdf	42199 f2fcc7c34c03d5e5026df23459ab62513bd13eff	no	1

Warnings:**Information:**

2	Fee Worksheet (PTO-875)	fee-info.pdf	30539 368a21707cfe6c9fd460ae48b0b47e88072a31b4	no	2
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Warnings:**Information:**

Total Files Size (in bytes):			72738
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New Applications Under 35 U.S.C. 111

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office

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